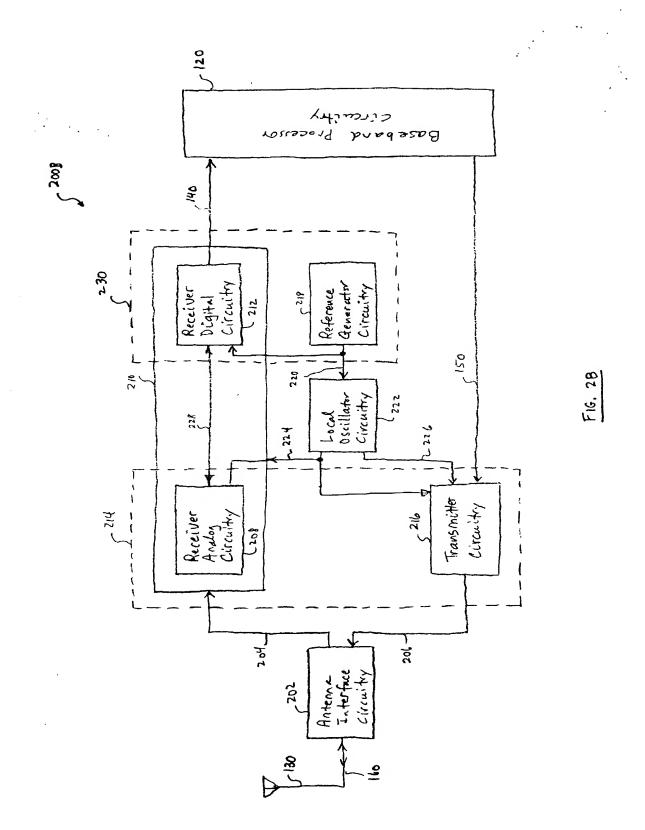
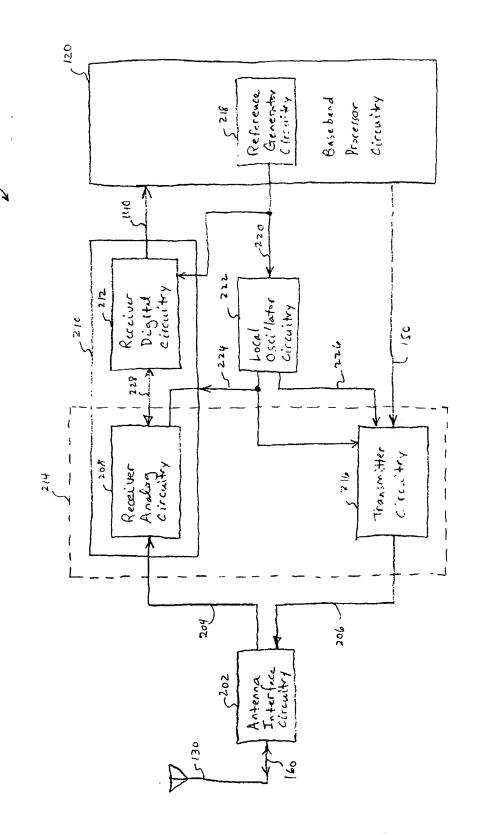


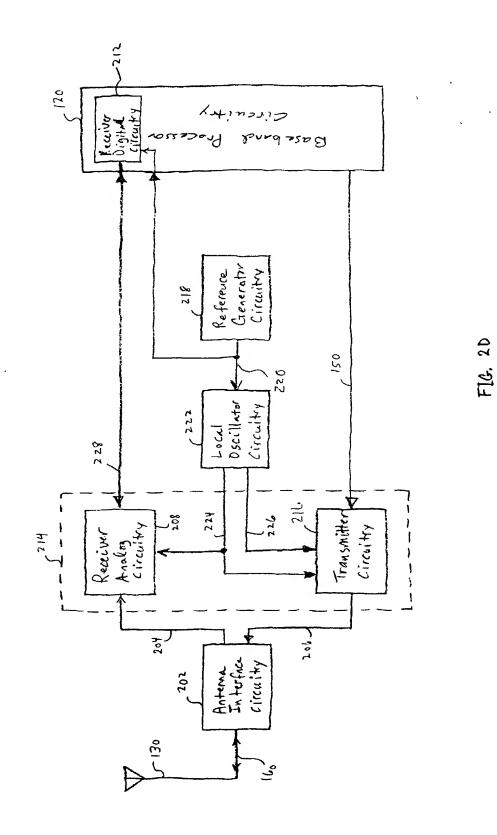
FIG. 24



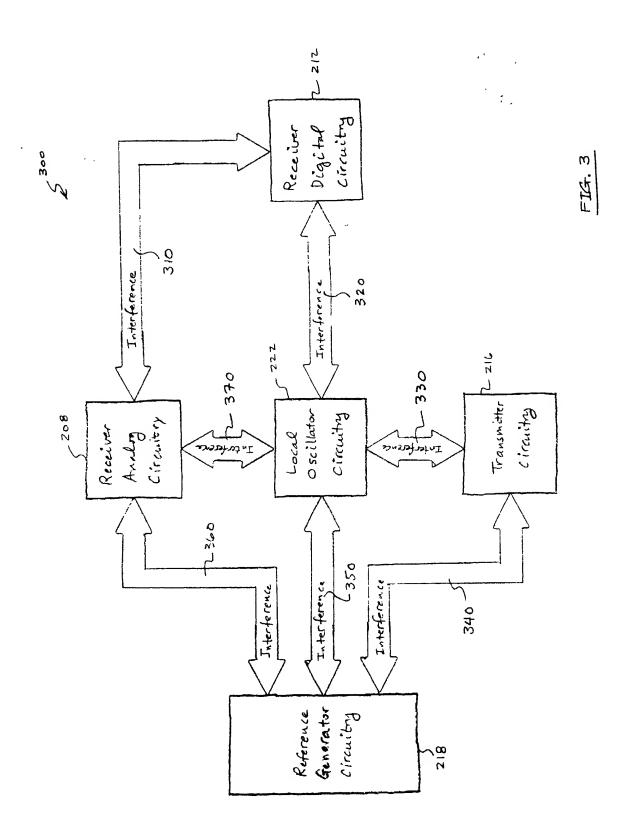


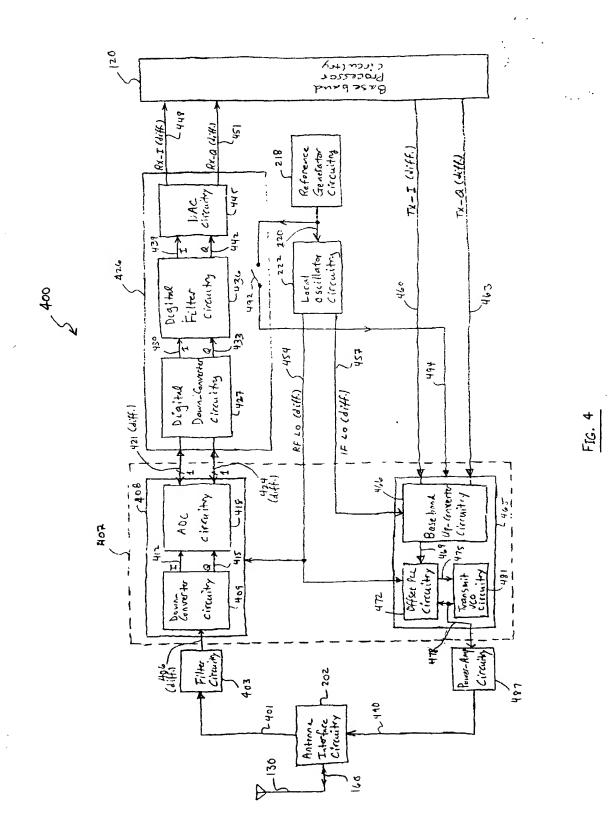
7005

FIG. 2C



J00 E





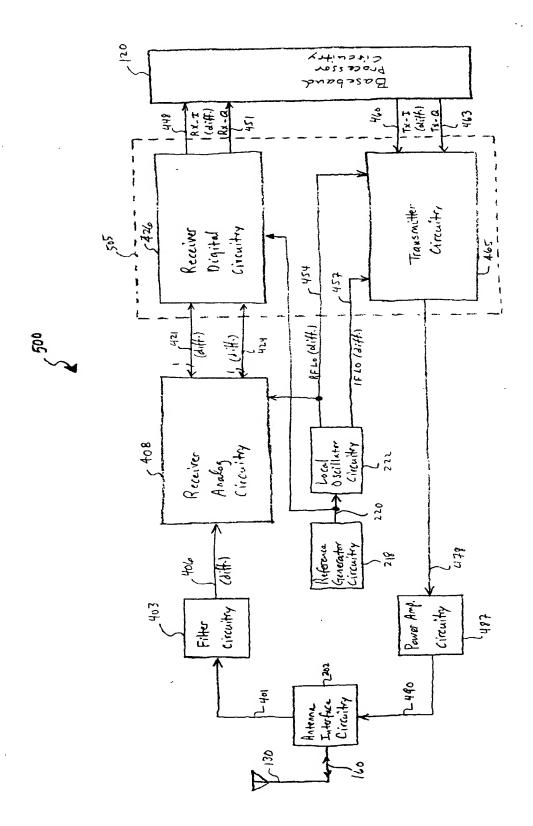


FIG. 5

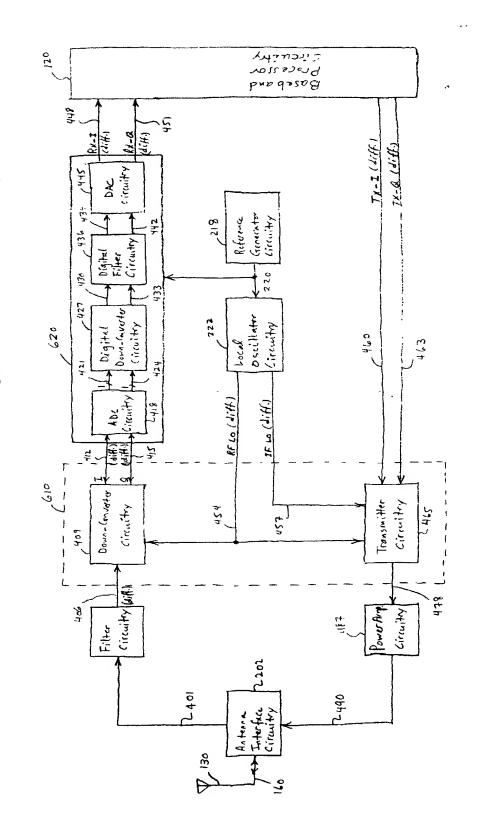
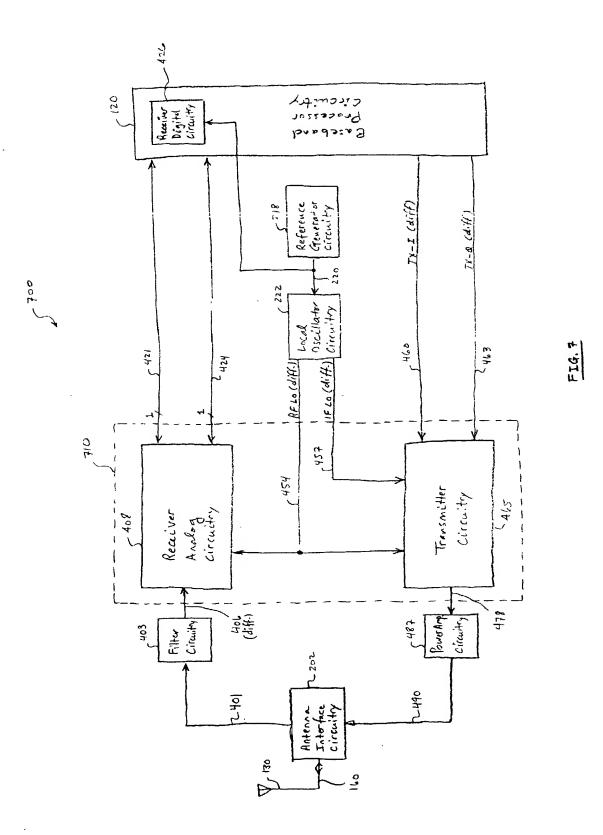
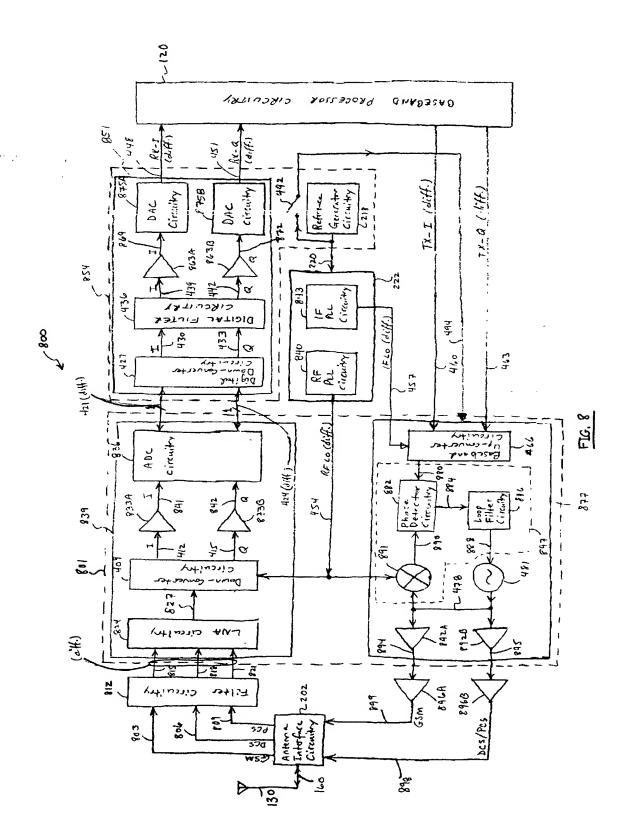
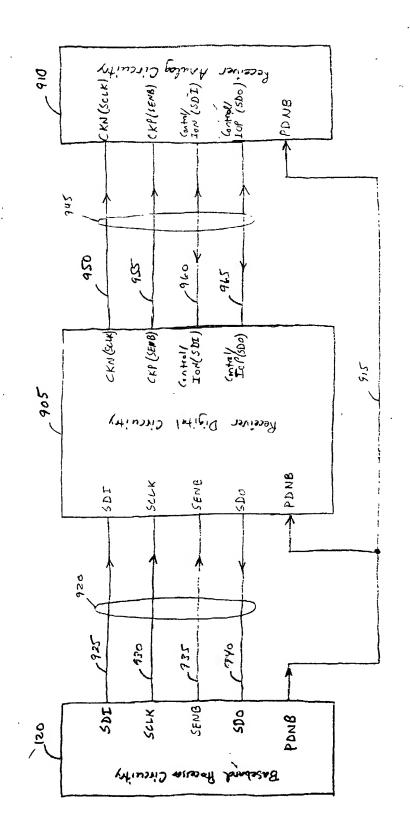


FIG. 6







2 900

FIG. 9A

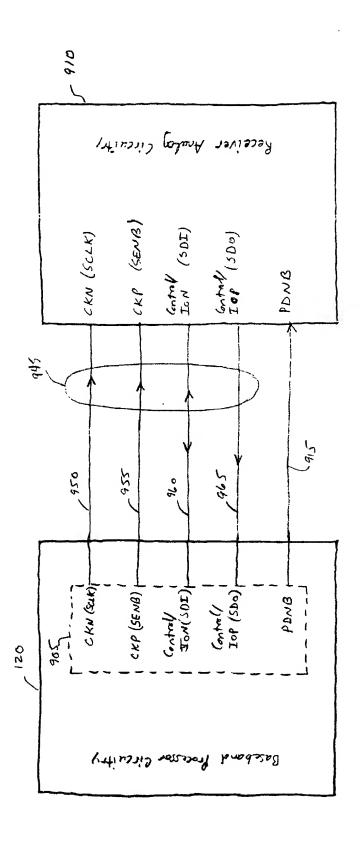
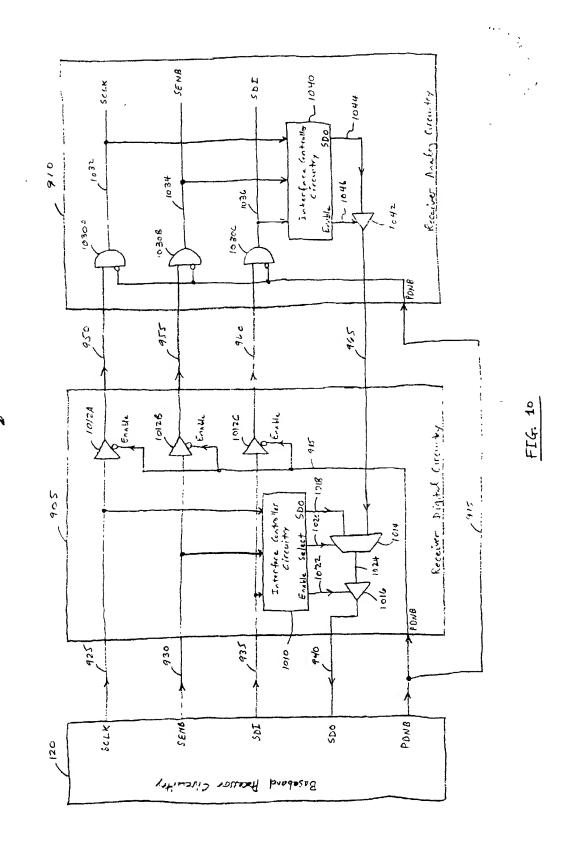
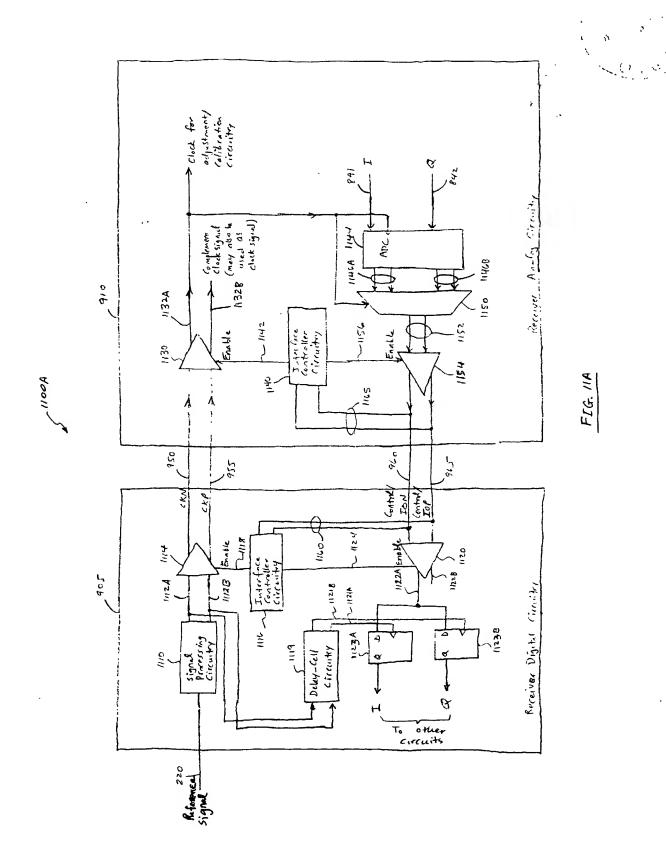
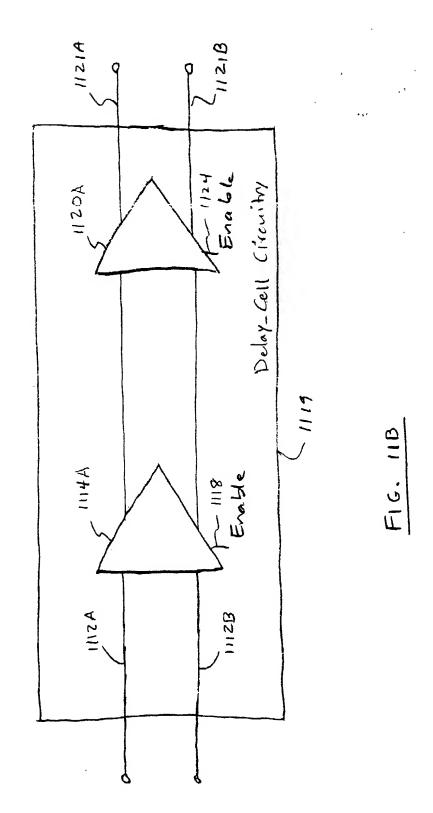


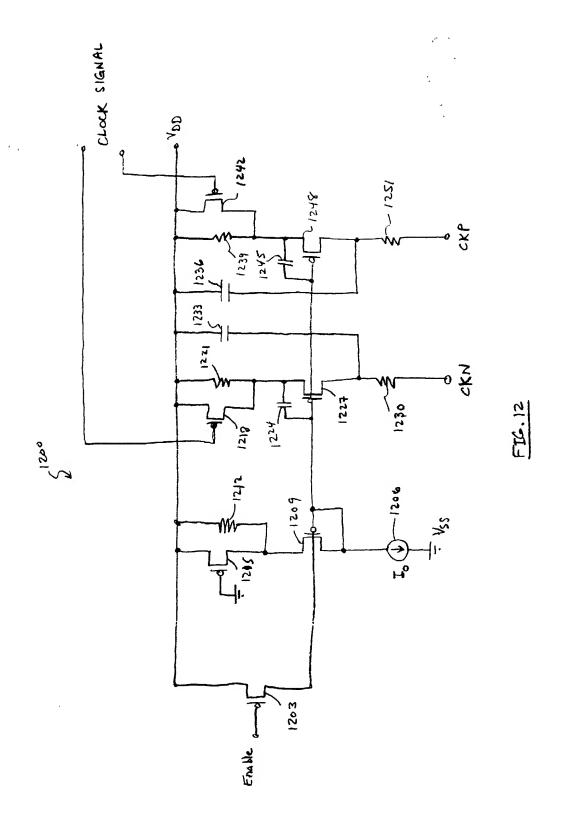
FIG. 98

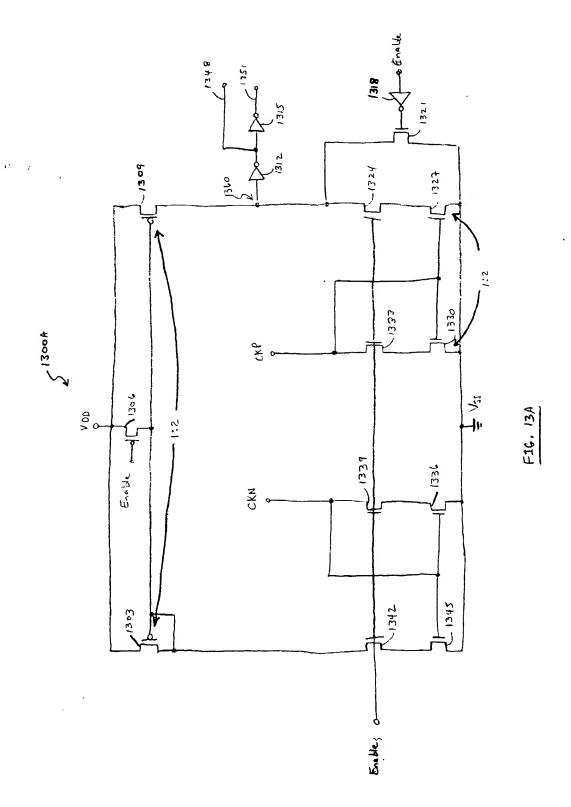
\$ 900B.

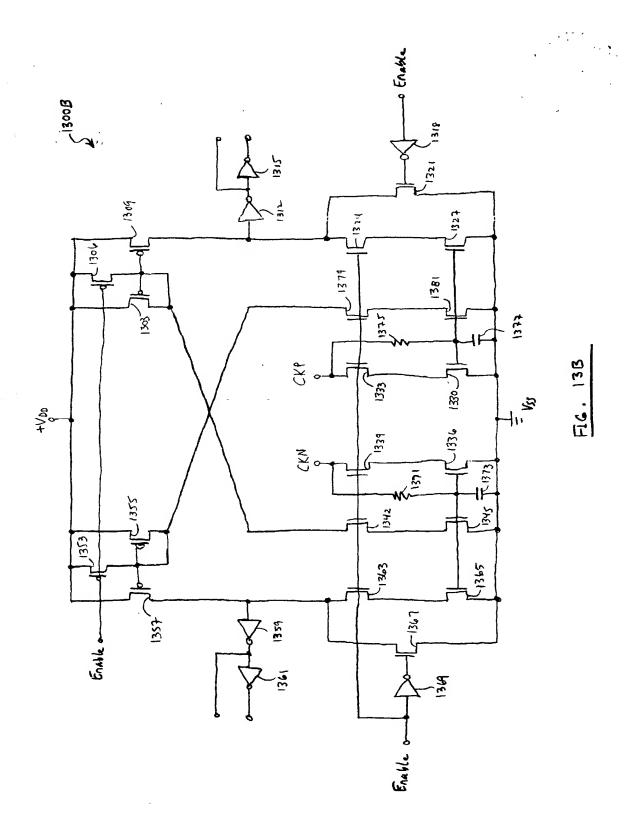


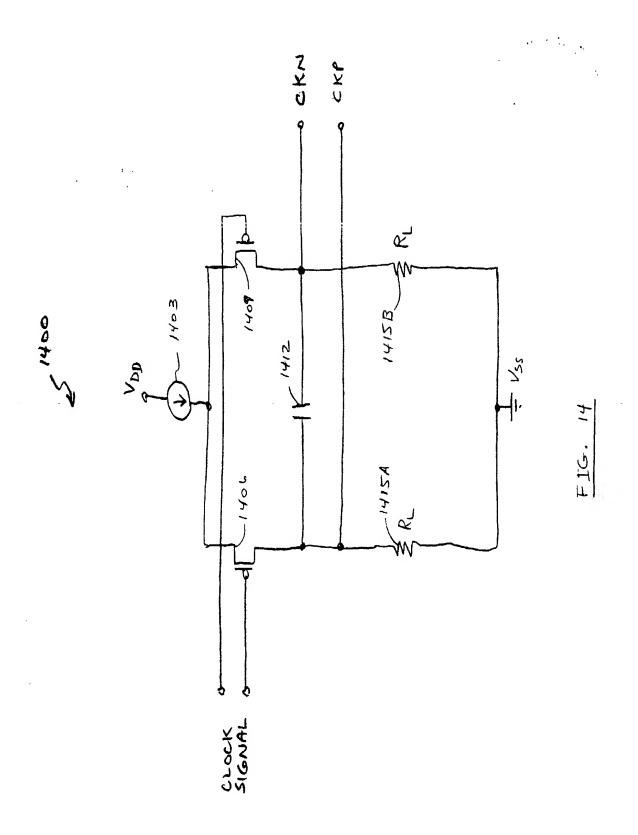


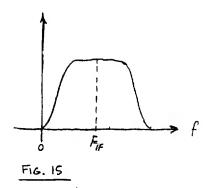


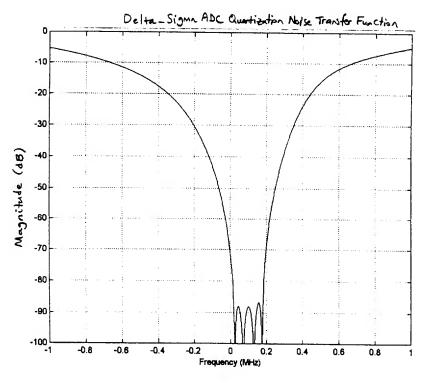




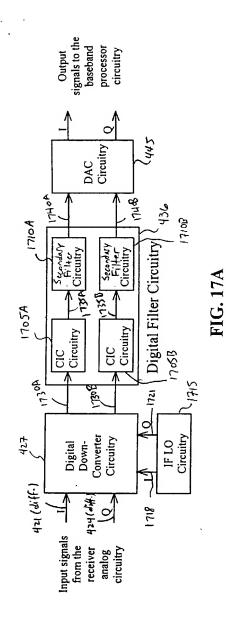




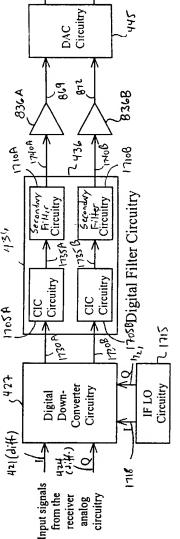




F16.16



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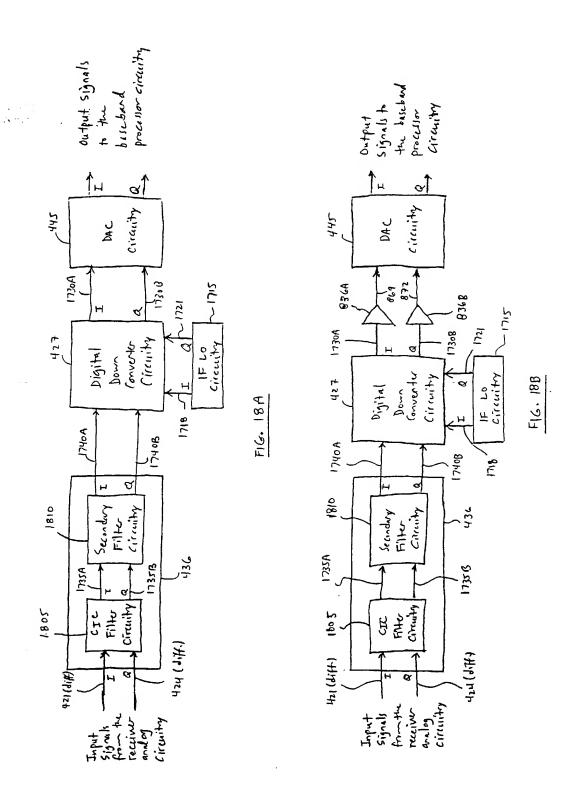


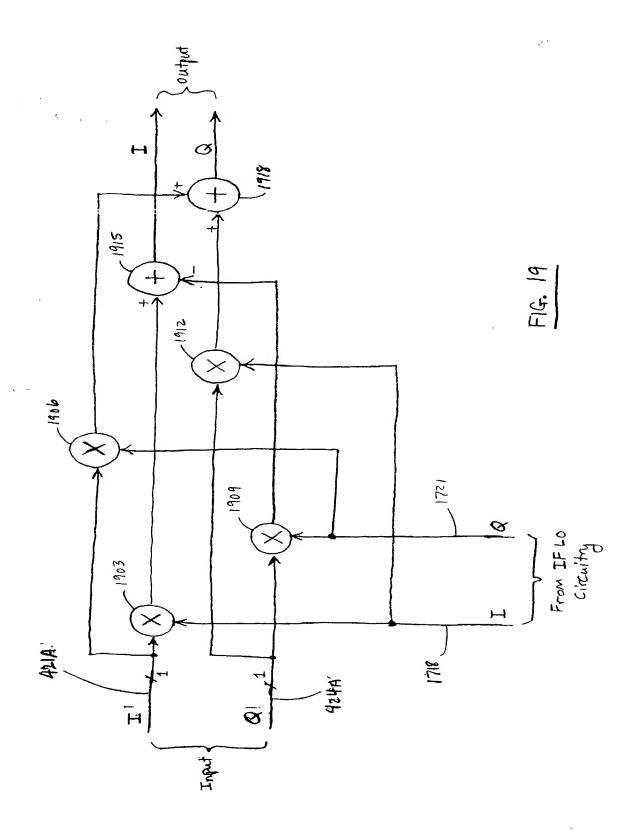
signals to the

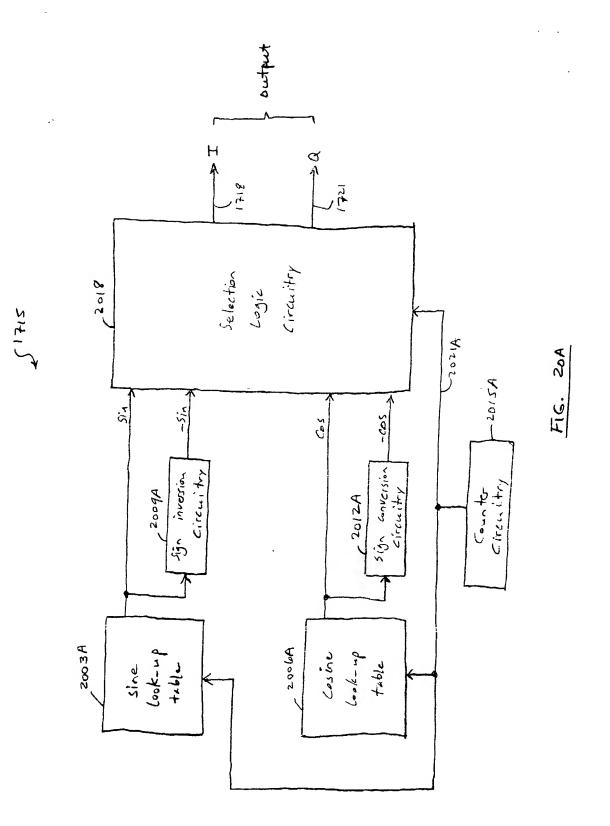
Output

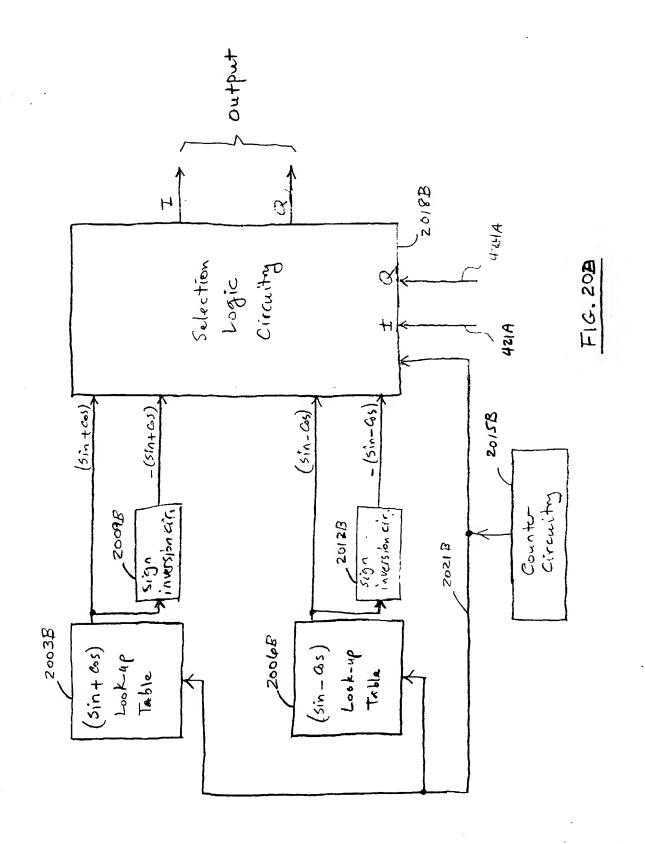
baseband processor circuitry

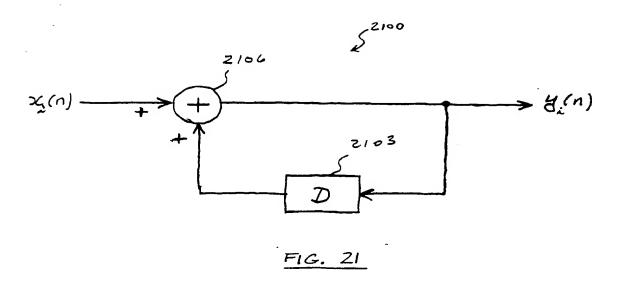
FIG. 17B

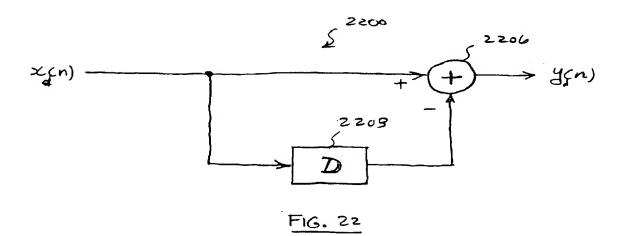


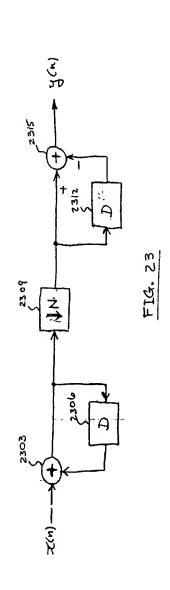


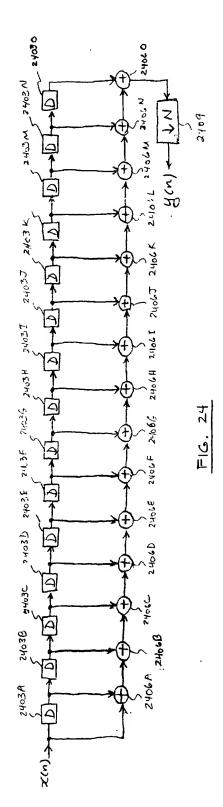


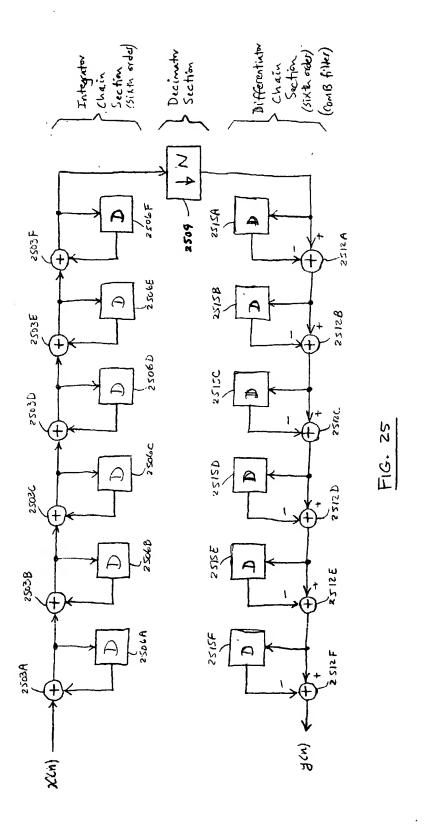


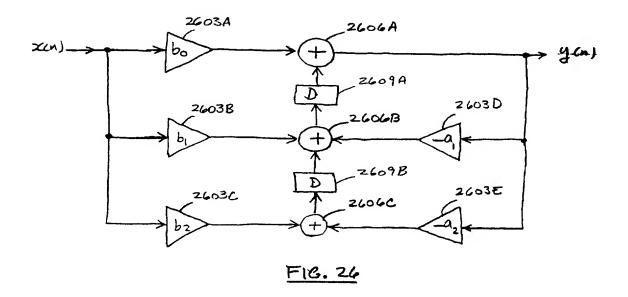












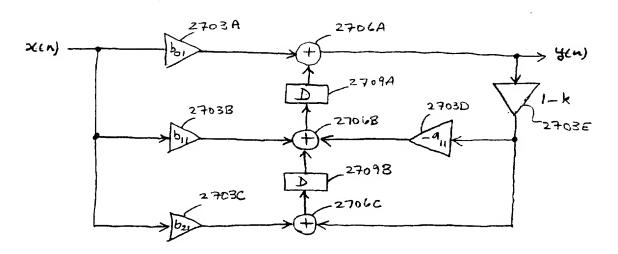
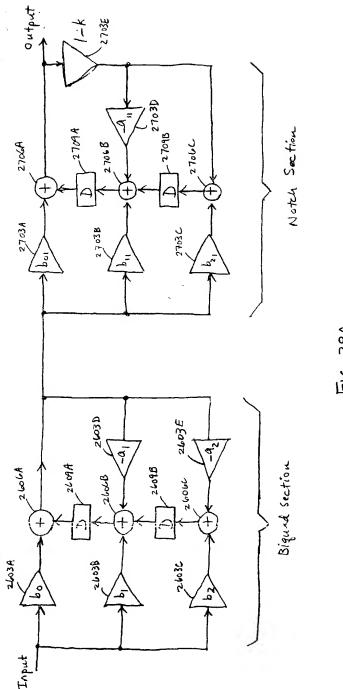
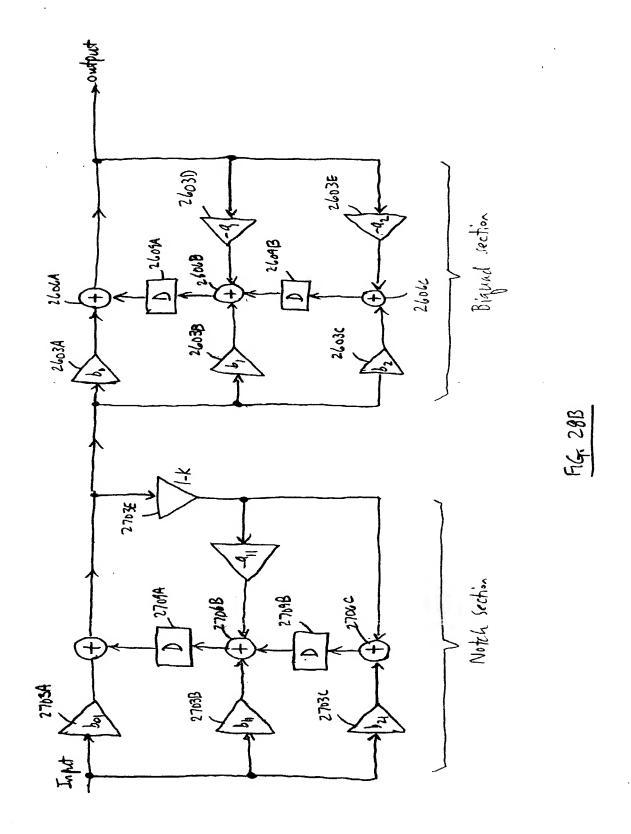
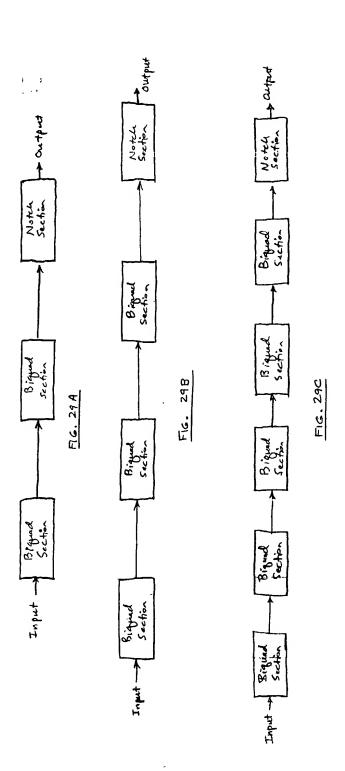
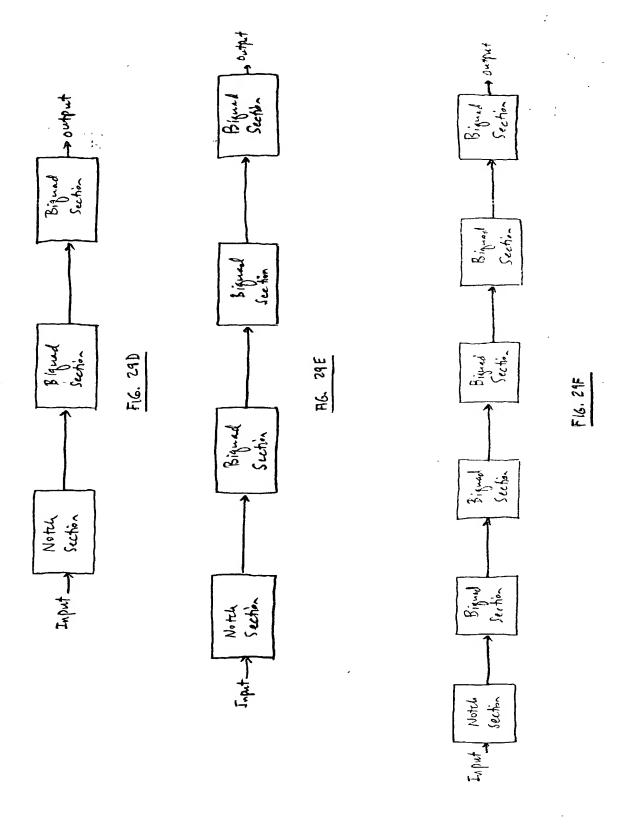


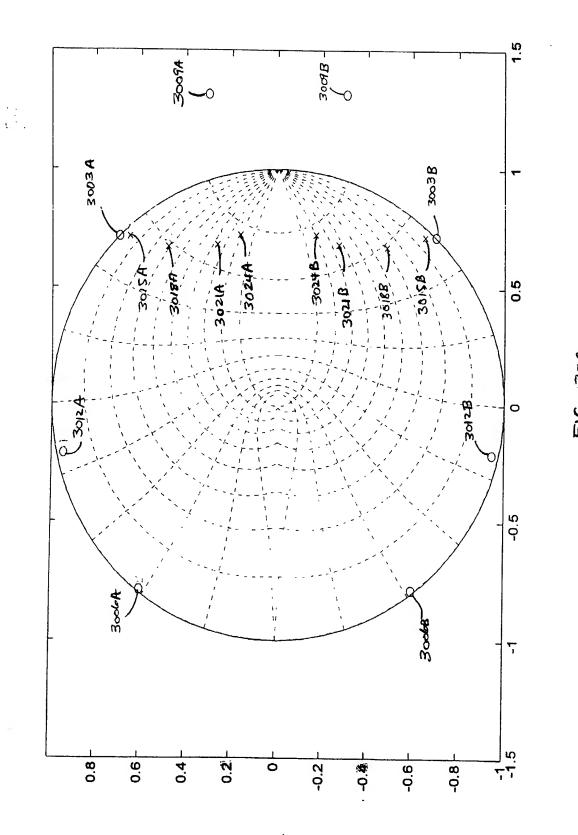
FIG. 27

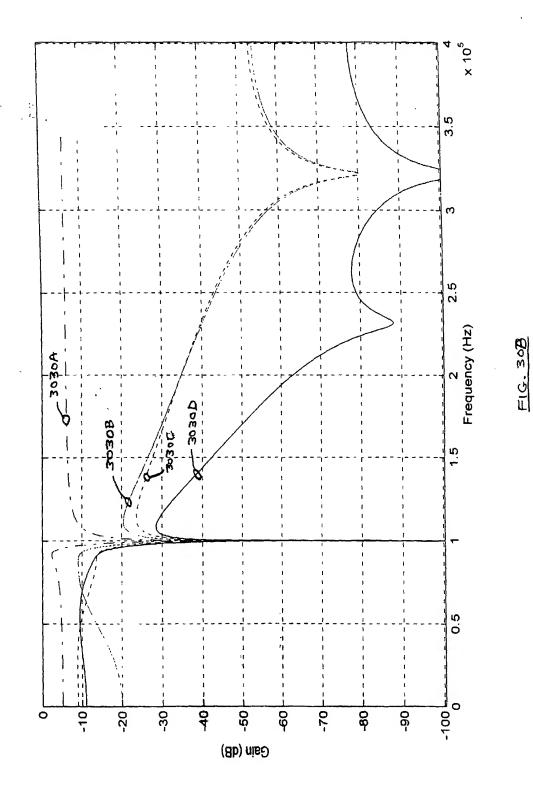


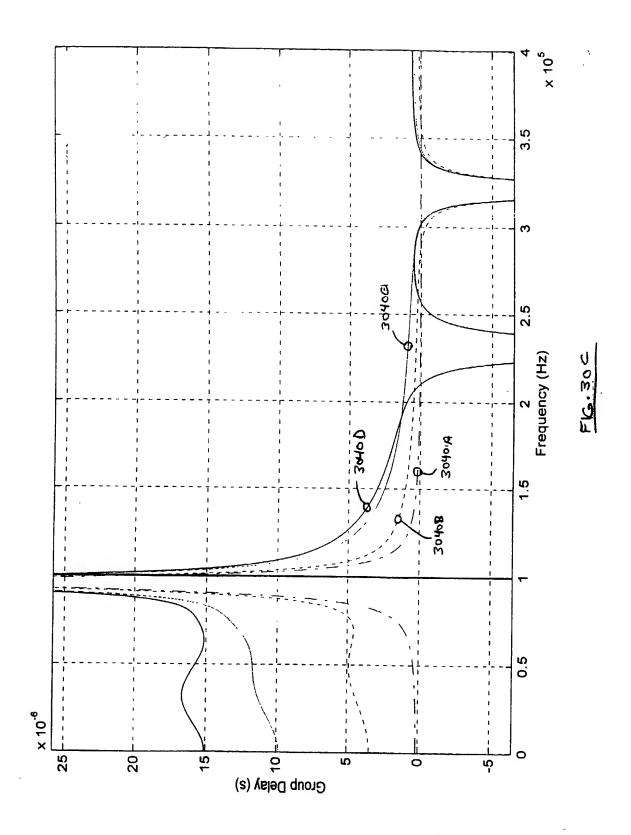


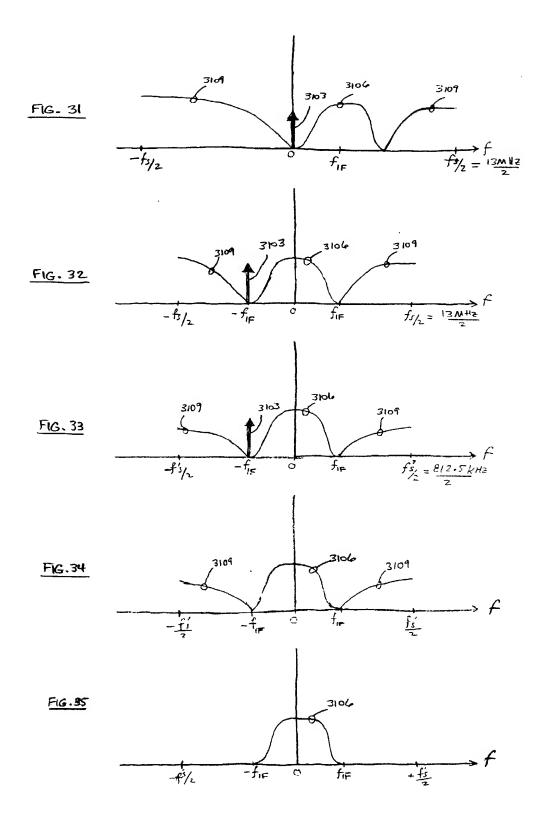


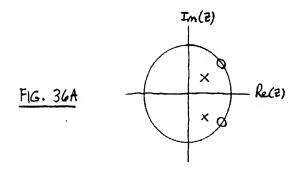


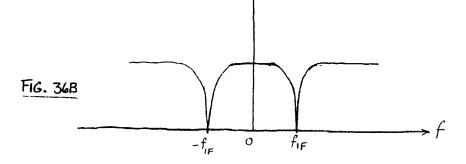


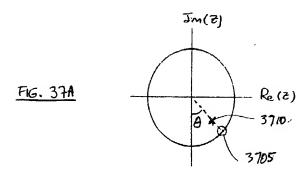


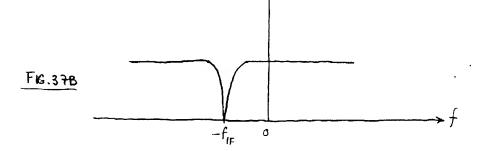












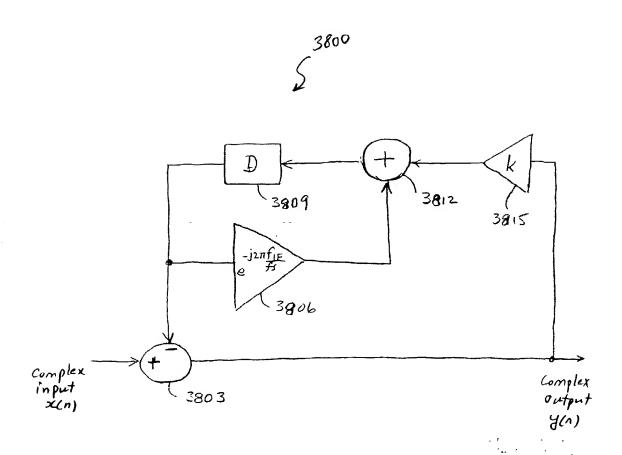


FIG. 38

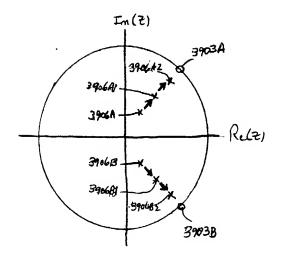
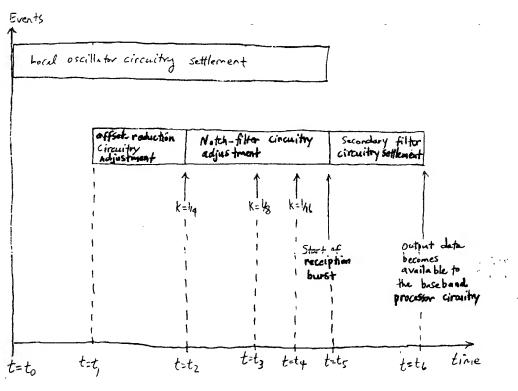


FIG. 39



FG. 40